

Special Invited Session 05

Micro/Nano Integrated Devices and Circuit Design

Micro/nano integrated devices and circuit design stands at the cutting - edge of modern electronics, merging micro-fabrication techniques with nano - scale material science to create high - performance, miniaturized systems. This session focuses on designing, fabricating, and optimizing devices and circuits at micro and nano dimensions, enabling breakthroughs in areas like 5G/6G communications, wearable electronics, and quantum computing.



Special Invited Session Chairs



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Paper Submission

For the initial submission, the authors can select one of the following two types:

- Type 1: full paper (4-6 pages)
- Type 2: extended abstract (2 pages)

*Note:

- View more on website:

<https://www.ieee-nems2026.org/initial-submission.html>

- Submit your paper through the following link or QR code:

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- Step 1: Please select the track “**Special Invited Sessions**” and click “**Continue**.”
- Step 2: After filling in the basic paper information, please select the topic “**SIS05 – Micro/Nano Integrated Devices and Circuit Design**.”



Important Dates

Initial Submission (Full Paper or Extended Abstract)	Jan. 31, 2026
Notification of Acceptance	Feb. 10, 2026
Late-News Submission Deadline	Feb. 28, 2026
Early Registration Deadline	Mar. 10, 2026
Presentation-Only Submission Deadline	Mar. 10, 2026
Final Submission Deadline	Mar. 10, 2026

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